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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/993,967	11/27/2001	Takashi Yamada	P 28403201F171	8085	
75	590 01/05/2004		EXAMINER		
Pillsbury Winthrop LLP			LEWIS, MONICA		
Intellectual Pro P O BOX 1050			ART UNIT PAPER NUMBER		
McLean, VA	McLean, VA 22102			2822	
			DATE MAILED: 01/05/200	1	

Please find below and/or attached an Office communication concerning this application or proceeding.

9A4

	Application No.	Applicant(s)	
Advisory Action	09/993,967	YAMADA ET AL.	
	Examiner	Art Unit	
	Monica Lewis	2822	
The MAILING DATE of this communication appe	ears on the cover sheet with the c	correspondence add	ress
THE REPLY FILED 10 November 2003 FAILS TO PLAGE Therefore, further action by the applicant is required to a final rejection under 37 CFR 1.113 may only be either: (condition for allowance; (2) a timely filed Notice of Appe Examination (RCE) in compliance with 37 CFR 1.114.	void abandonment of this application (1) a timely filed amendment whi	cation. A proper report of the contract of the	ply to a cation in
PERIOD FOR RE	EPLY [check either a) or b)]		
a) The period for reply expires 3 months from the mailing date of b) The period for reply expires on: (1) the mailing date of this Advevent, however, will the statutory period for reply expire later the ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS 706.07(f). Extensions of time may be obtained under 37 CFR 1.136(a). The dath have been filed is the date for purposes of determining the period of extensions of the calculated from: (1) the expiration date of the shortened (b) above, if checked. Any reply received by the Office later than three moleaned patent term adjustment. See 37 CFR 1.704(b).	risory Action, or (2) the date set forth in the an SIX MONTHS from the mailing date of FILED WITHIN TWO MONTHS OF THE te on which the petition under 37 CFR 1.1 sion and the corresponding amount of the I statutory period for reply originally set in	f the final rejection. E FINAL REJECTION. S I 36(a) and the appropriate e fee. The appropriate ext the final Office action; or	See MPEP e extension fee tension fee under (2) as set forth in
1. A Notice of Appeal was filed on Appellant' 37 CFR 1.192(a), or any extension thereof (37 CF			
2. The proposed amendment(s) will not be entered b	ecause:		
(a) \(\square\) they raise new issues that would require furth	er consideration and/or search ((see NOTE below);	
(b) they raise the issue of new matter (see Note I	pelow);		
(c) they are not deemed to place the application issues for appeal; and/or	in better form for appeal by mat	erially reducing or s	simplifying the
(d) they present additional claims without cancel NOTE:	ing a corresponding number of	finally rejected clair	ns.
3. Applicant's reply has overcome the following rejection	ction(s):		
4. Newly proposed or amended claim(s) would canceling the non-allowable claim(s).	be allowable if submitted in a s	eparate, timely filed	d amendment
5. ☐ The a) ☐ affidavit, b) ☐ exhibit, or c) ☐ request fo application in condition for allowance because: See		sidered but does NC	OT place the
6. The affidavit or exhibit will NOT be considered be raised by the Examiner in the final rejection.	cause it is not directed SOLELY	to issues which we	re newly
7. For purposes of Appeal, the proposed amendment explanation of how the new or amended claims w			and an
The status of the claim(s) is (or will be) as follows:			
Claim(s) allowed:			
Claim(s) objected to:			
Claim(s) rejected:			
Claim(s) withdrawn from consideration:			
8. The drawing correction filed on is a) app	proved or b) disapproved by	the Examiner.	
9. Note the attached Information Disclosure Stateme	nt(s)(PTO-1449) Paper No(s).		
10. Other:			
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C ntinuati n Sheet (PTOL-303) 09/993,967

Application No.

Continuation of 5. does NOT place the application in condition for allowance because. First, Applicant argues that "Lee does not teach a groove which extends into the dielectric." Lee teaches that the "trench isolation region 16 is formed in a semiconductor substrate 10 and that the dielectric film 20 is formed on an inner wall of the trenches." However, Lee teaches that a groove (16) that extends into the dielectric (20). According to Merriam-Webster a groove is defined as a long narrow channel which (16) exemplifies. Second, Applicant argues that "Lee does not teach that an impurity layer is formed to be in contact with the bottom surface of the semiconductor layer. However, Merriam-Webster defines contact as to join and join in is defined as to put or bring in to close association or relationship. Therefore, the impurity diffusion source (32) is formed to be in contact with the bottom surface of the semiconductor layer. There is no language stating that they are in "direct" contact. Finally, Applicant argues that "Lee does not teach that the groove has an increased width portion which is receded laterally as to expose a bottom surface of the semiconductor layer such that the width of the groove in the dieletric film is greater than that of the groove in the semiconductor layer." However, Malik discloses the limitations disclosed above (For Example: See Figure 10)